

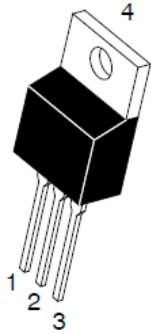


# TSR30V60CT TSR30V60CTF

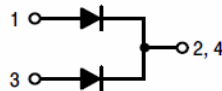
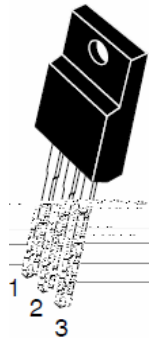
19-NOV-2013

## Trench MOS Barrier Schottky Rectifier

**TSR30V60CT**  
**HC!**



**TSR30V60CTF**  
**HC!**



### Features

- Advanced trench technology
- Low forward voltage drop
- Low power losses
- High efficiency operation
- Lead Free Finish, RoHS Compliant

### Applications

- DC/DC Converters
- AC/DC Adaptors
- Switching Power Supplies
- Freewheeling Diodes

### Maximum ratings and electrical characteristics (T<sub>J</sub> = 25°C unless otherwise noted)

Parameter		Symbol	Limit		Unit	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	60		V	
Maximum average forward rectified current	per device	I <sub>F(AV)</sub>	30		A	
	per diode		15			
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I <sub>FSM</sub>	280		A	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-40 to +150		°C	
Typical thermal resistance per leg	TO-220	R <sub>JC</sub>	2		°C/W	
	TO-220Ø		4		°C/W	
Instantaneous forward voltage per diode	I <sub>F</sub> =5A	T <sub>J</sub> =25°C	TYP.	MAX.	V	
			0.38	-		
	I <sub>F</sub> =15A		T <sub>J</sub> =125°C	0.34		-
			0.45	0.48		
Instantaneous reverse current per diode at rated reverse voltage	T <sub>J</sub> =25°C	I <sub>R(2)</sub>	-	50	µA	
	T <sub>J</sub> =125°C		30	-	mA	

#### Notes:

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

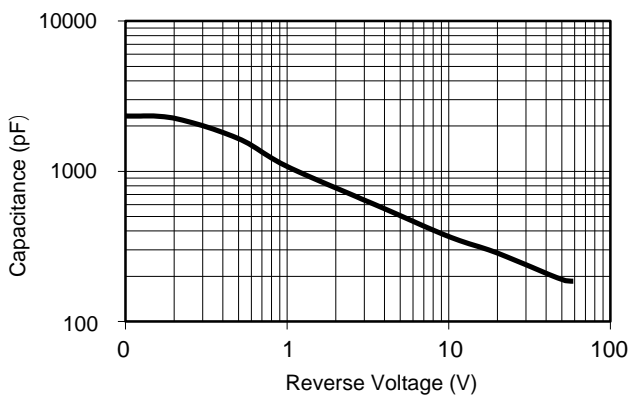
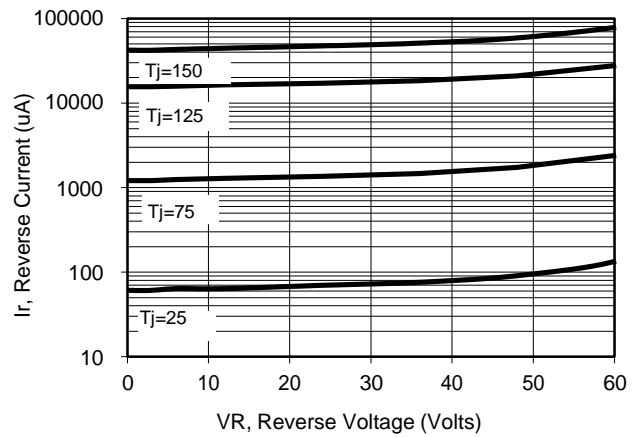
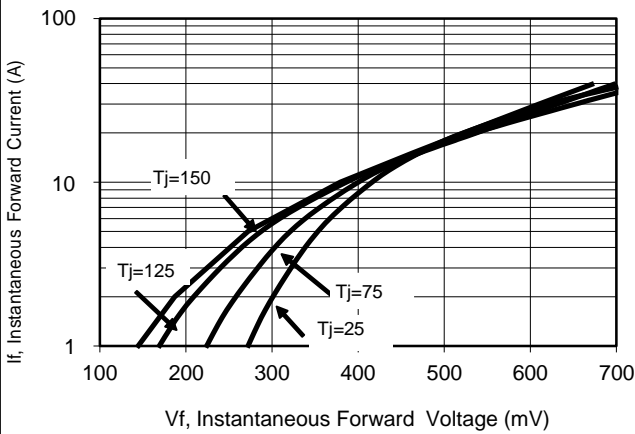
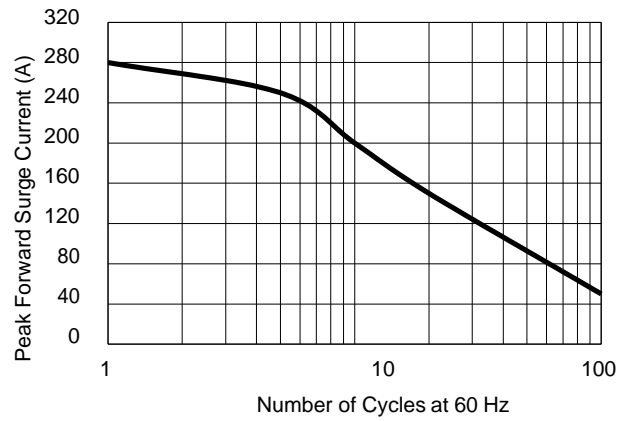
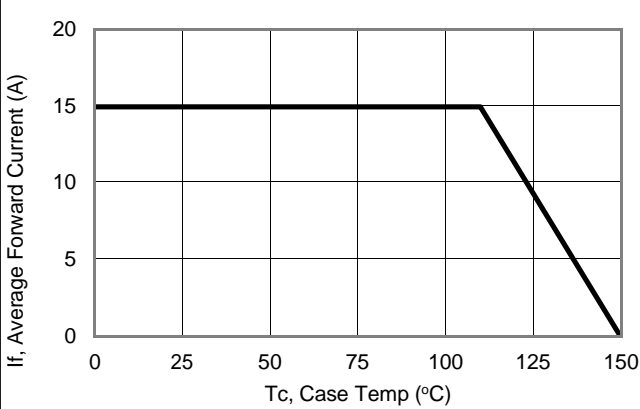
(2) Pulse test: Pulse width ≤ 40 ms



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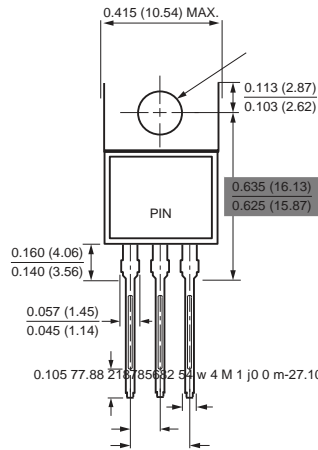
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## RATINGS AND CHARACTERISTICS CURVES (TA = 25 °C unless otherwise noted)



# PACKAGE OUTLINE DIMENSIONS

TO-220AB



TO-220F

